

02/06/2008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.	10/711,959	Conf. No.	5958
In Re Application of:	Heng et al.	Art Unit:	2825
Filed:	10/15/2004	Dkt. #:	BUR920040201US1 (IBMB-0059)
Title:	INTEGRATED CIRCUIT SELECTIVE SCALING	Examiner:	Kik, Phallaka

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. 1.312

Sir:

I. INTRODUCTORY COMMENTS:

This paper is being filed after a Notice of Allowance but before payment of the Issue Fee. Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 6 of this paper.